Trench-Based Schottky Rectifier, Low Leakage

Features

- Fine Lithography Trench-based Schottky Technology for Very Low Forward Voltage and Low Leakage
- Fast Switching with Exceptional Temperature Stability
- Low Power Loss and Lower Operating Temperature
- Higher Efficiency for Achieving Regulatory Compliance
- High Surge Capability
- NRV Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements; AEC-Q101 Qualified and PPAP Capable
- These are Pb-Free and Halide-Free Devices

Typical Applications

- Switching Power Supplies including Wireless, Smartphone and Notebook Adapters
- High Frequency and DC-DC Converters
- Freewheeling and OR-ing diodes
- Reverse Battery Protection
- Instrumentation
- LED Lighting

Mechanical Characteristics:

- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94-0 @ 0.125 in.
- Lead Finish: 100% Matte Sn (Tin)
- Lead and Mounting SurfaceTemperature for Soldering Purposes: 260°C Max. for 10 Seconds

1

• Device Meets MSL 1 Requirements



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SCHOTTKY BARRIER RECTIFIERS 4 AMPERES 100 VOLTS

MARKING DIAGRAMS



SMA CASE 403D STYLE 1



TE41 = Specific Device Code A = Assembly Location

Y = Year
WW = Work Week

Pb-Free Package

(Note: Microdot may be in either location)

ORDERING INFORMATION

Device	Package	Shipping†
NRVTSA4100ET3G	SMA (Pb-Free)	5000 / Tape & Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	100	V
Average Rectified Forward Current (T _L = 142°C)	I _{F(AV)}	4.0	А
Peak Repetitive Forward Current, (Square Wave, 20 kHz, T _L = 135°C)	I _{FRM}	8.0	А
Non-Repetitive Peak Surge Current (Surge Applied at Rated Load Conditions Halfwave, Single Phase, 60 Hz)	I _{FSM}	150	А
Storage Temperature Range	T _{stg}	-65 to +175	°C
Operating Junction Temperature	TJ	-55 to +175	°C
ESD Rating (Human Body Model)		1B	
ESD Rating (Machine Model)		МЗ	

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance, Junction-to-Lead, Steady State (Assumes 600 mm² 1 oz. copper bond pad, on a FR4 board)	$R_{ heta JL}$	-	16.2	°C/W
Thermal Resistance, Junction-to-Ambient, Steady State (Assumes 600 mm² 1 oz. copper bond pad, on a FR4 board)	$R_{\theta JA}$	-	90	°C/W

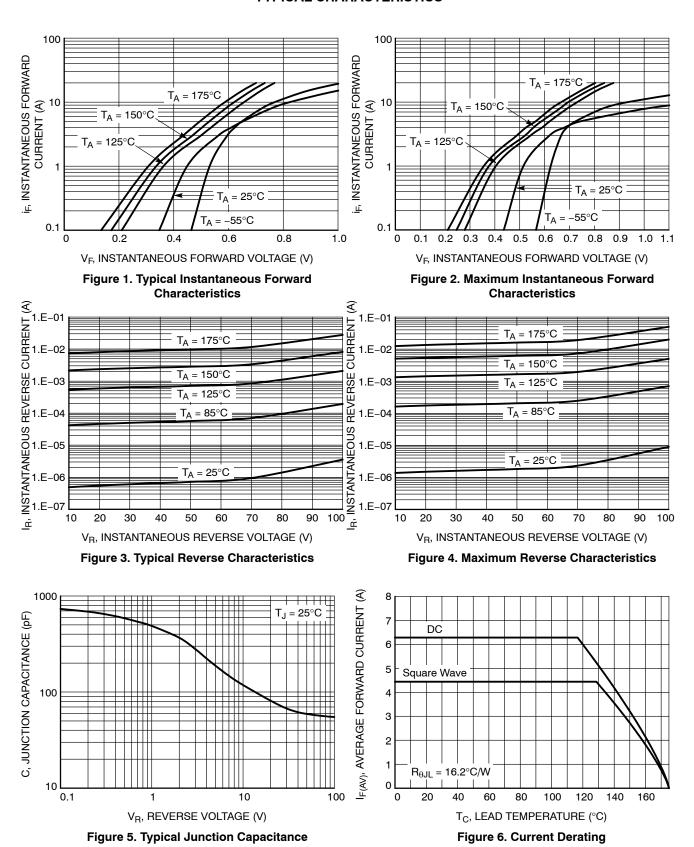
ELECTRICAL CHARACTERISTICS

Instantaneous Forward Voltage (Note 1)	٧ _F			V
$(i_F = 1.0 \text{ Amps}, T_J = 25^{\circ}\text{C})$		0.45	-	
$(i_F = 4.0 \text{ Amps}, T_J = 25^{\circ}\text{C})$		0.61	0.68	
(i 10 Amno T 105°C)		0.36		
$(i_F = 1.0 \text{ Amps}, T_J = 125^{\circ}\text{C})$			-	
$(i_F = 4.0 \text{ Amps}, T_J = 125^{\circ}\text{C})$		0.53	0.59	
Reverse Current (Note 1)	i _R			
(Rated dc Voltage, T _J = 25°C)		3.5	29	μΑ
(Rated dc Voltage, T _J = 125°C)		2.0	5.0	mA
Diode Capacitance	C _d			pF
(Rated dc Voltage, T _J = 25°C, f = 1 MHz)		55		

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

1. Pulse Test: Pulse Width = 300 µs, Duty Cycle ≤ 2.0%.

TYPICAL CHARACTERISTICS



TYPICAL CHARACTERISTICS

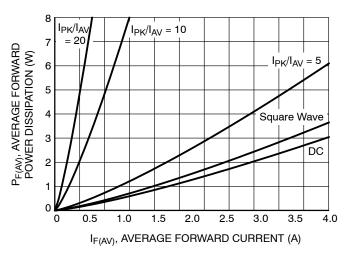


Figure 7. Forward Power Dissipation

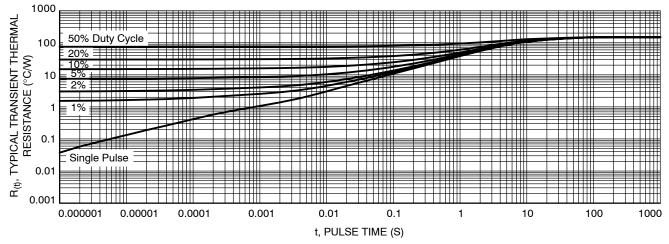


Figure 8. Typical Transient Thermal Response, Junction-to-Ambient

MECHANICAL CASE OUTLINE

PACKAGE DIMENSIONS





HE

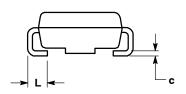
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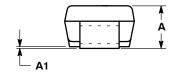
SMA CASE 403D ISSUE H

DATE 23 SEP 2015

- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M,
- 1982. CONTROLLING DIMENSION: INCH.
- DIMENSION b SHALL BE MEASURED WITHIN DIMENSION L.

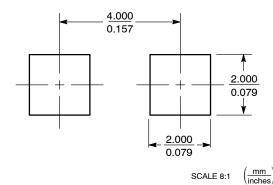
	MILLIMETERS				INCHES	
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	1.97	2.10	2.20	0.078	0.083	0.087
A1	0.05	0.10	0.20	0.002	0.004	0.008
b	1.27	1.45	1.63	0.050	0.057	0.064
С	0.15	0.28	0.41	0.006	0.011	0.016
D	2.29	2.60	2.92	0.090	0.103	0.115
E	4.06	4.32	4.57	0.160	0.170	0.180
HE	4.83	5.21	5.59	0.190	0.205	0.220
L	0.76	1.14	1.52	0.030	0.045	0.060





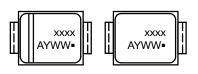
SOLDERING FOOTPRINT*

POLARITY INDICATOR OPTIONAL AS NEEDED (SEE STYLES)



^{*}For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

GENERIC MARKING DIAGRAM*



STYLE 1

STYLE 2

= Specific Device Code XXXX = Assembly Location Α

Υ = Year ww = Work Week = Pb-Free Package

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot " ■", may or may not be present.

STYLE 1: PIN 1. CATHODE (POLARITY BAND)

STYLE 2: NO POLARITY

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