45 V, 500 mA PNP general-purpose transistors Rev. 7 — 15 June 2018

**Product data sheet** 

#### **Product profile** 1

#### **1.1 General description**

PNP general-purpose transistors in a small SOT23 (TO-236AB) Surface-Mounted Device (SMD) plastic package.

	Table 1	<b>Product</b>	overview
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Type number	Package		NPN	
	Nexperia	JEDEC	JEITA	complement
BC807	SOT23	TO-236AB	-	BC817
BC807-16				BC817-16
BC807-25				BC817-25
BC807-40				BC817-40

#### 1.2 Features and benefits

- High current
- Three current gain selections
- AEC-Q101 qualified

#### **1.3 Applications**

· General-purpose switching and amplification



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### 1.4 Quick reference data

#### Table 2. Quick reference data

*T<sub>amb</sub>* = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-	-45	V
I <sub>C</sub>	collector current			-	-	-500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-	-1	А
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA					
	BC807		[1]	100	-	600	
	BC807-16		[1]	100	-	250	
	BC807-25		[1]	160	-	400	
	BC807-40	_	[1]	250	-	600	

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 

### 2 Pinning information

Table 3. Pinning				
Pin	Symbol	Description	Simplified outline	Graphic symbol
SOT23		·		
1	В	base		
2	E	emitter	3	C I
3	C	collector		B E sym132

### **3** Ordering information

#### Table 4. Ordering information

Type number	Package					
	Name	Description	Version			
BC807	TO-236AB	Plastic surface-mounted package; 3 leads	SOT23			
BC807-16						
BC807-25						
BC807-40						

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#### Marking 4

Table 5. Marking						
Type number		Marking code				
BC807	[1]	5D%				
BC807-16	[1]	5A%				
BC807-25	[1]	5B%				
BC807-40	[1]	5C%				

[1] % = placeholder for manufacturing site code

#### **Limiting values** 5

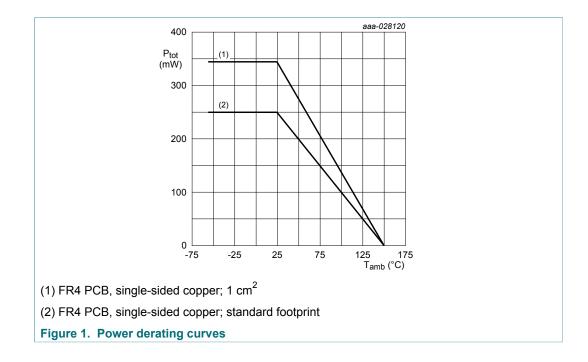
#### Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).  $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Max	Unit
V <sub>CBO</sub>	collector-base voltage	open emitter		-	-50	V
V <sub>CEO</sub>	collector-emitter voltage	open base		-	-45	V
V <sub>EBO</sub>	emitter-base voltage	open collector		-	-5	V
l <sub>C</sub>	collector current			-	-500	mA
I <sub>CM</sub>	peak collector current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-1	А
I <sub>BM</sub>	peak base current	single pulse; t <sub>p</sub> ≤ 1 ms		-	-200	mA
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> ≤ 25 °C	[1] [2]	-	250	mW
			[3] [2]	-	345	mW
Tj	junction temperature			-	150	°C
T <sub>amb</sub>	ambient temperature			-65	150	°C
T <sub>stg</sub>	storage temperature			-65	150	°C

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

[2] Valid for all available selection groups.
[3] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.



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#### **Thermal characteristics** 6

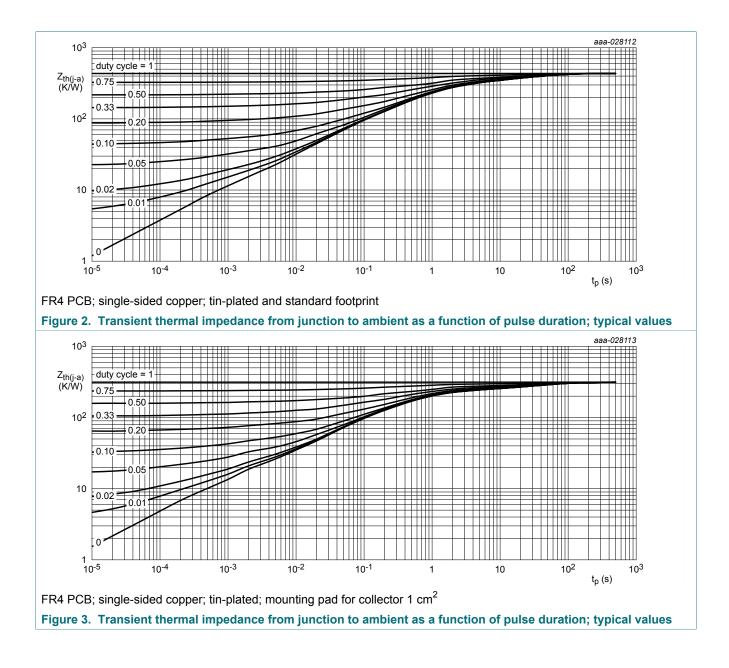
#### Table 7. Thermal characteristics

 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
R <sub>th(j-a)</sub>	thermal resistance from junction	in free air	[1] [2]	-	-	500	K/W
	to ambient		[3] [2]	-	-	362	K/W

[1] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated and standard footprint.

[2] Valid for all available selection groups.
 [3] Device mounted on an FR4 Printed-Circuit-Board (PCB); single-sided copper; tin-plated; mounting pad for collector 1 cm<sup>2</sup>.



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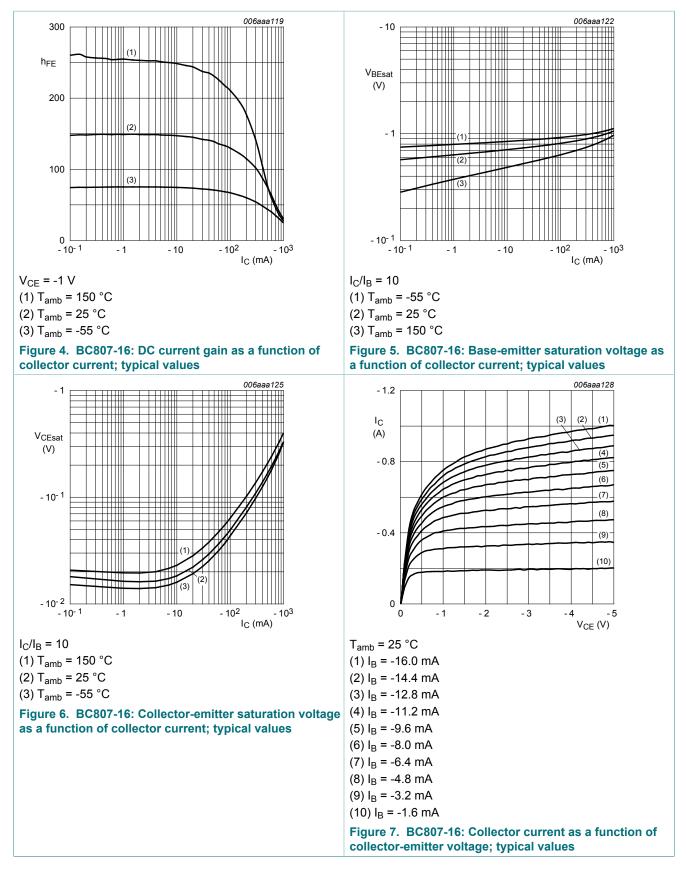
#### **Characteristics** 7

#### Table 8. Characteristics

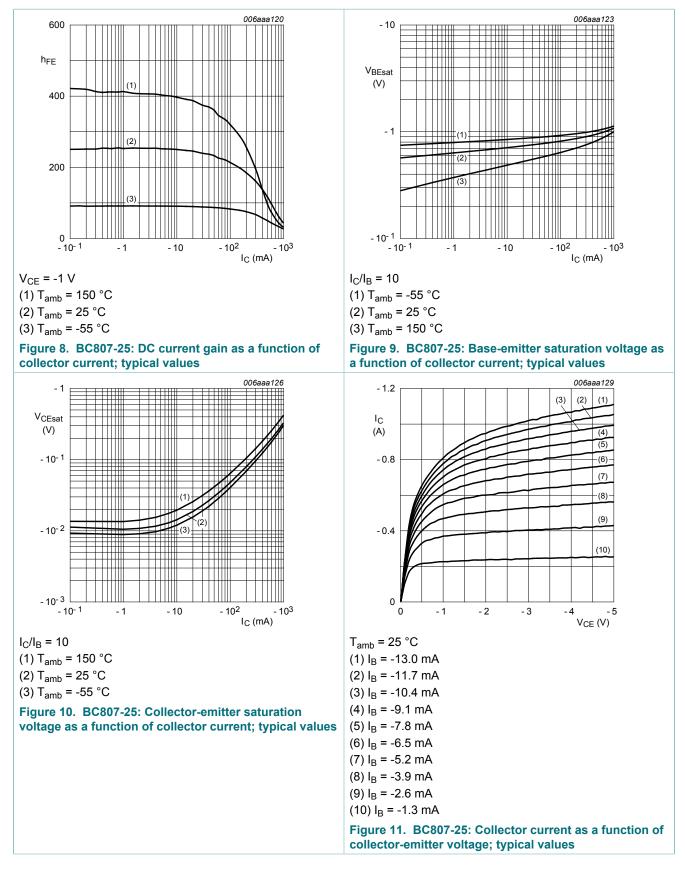
 $T_{amb}$  = 25 °C unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Мах	Unit
V <sub>(BR)CBO</sub>	collector-base breakdown voltage	I <sub>C</sub> = -100 μA; I <sub>E</sub> = 0 A		-50	-	-	V
V <sub>(BR)CEO</sub>	collector-emitter breakdown voltage	I <sub>C</sub> = -10 mA; I <sub>B</sub> = 0 A		-45	-	-	V
V <sub>(BR)EBO</sub>	emitter-base breakdown voltage	I <sub>E</sub> = -100 μA; I <sub>C</sub> = 0 A	I <sub>E</sub> = -100 μA; I <sub>C</sub> = 0 A		-	-	V
I <sub>CBO</sub>	collector-base	V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A		-	-	-100	nA
	cut-off current	V <sub>CB</sub> = -20 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C		-	-	-5	μA
I <sub>EBO</sub>	emitter-base cut-off current	$V_{EB} = -5 V; I_C = 0 A$		-	-	-100	nA
h <sub>FE</sub>	DC current gain			1			
	BC807	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA	[1]	100	-	600	
	BC807-16	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA	[1]	100	-	250	
	BC807-25	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA	[1]	160	-	400	
	BC807-40	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -100 mA	[1]	250	-	600	
h <sub>FE</sub>	DC current gain	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -500 mA	[1]	40	-	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = -500 mA; I <sub>B</sub> = -50 mA	[1]	-	-	-700	mV
V <sub>BE</sub>	base-emitter voltage	V <sub>CE</sub> = -1 V; I <sub>C</sub> = -500 mA	[1] [2]	-	-	-1.2	V
f <sub>T</sub>	transition frequency	$V_{CE}$ = -5 V; I <sub>C</sub> = -10 mA; f = 100 MHz		80	-	-	MHz
C <sub>c</sub>	collector capacitance	V <sub>CB</sub> = -10 V; I <sub>E</sub> = i <sub>e</sub> = 0 A; f = 1 MHz		-	5	-	pF

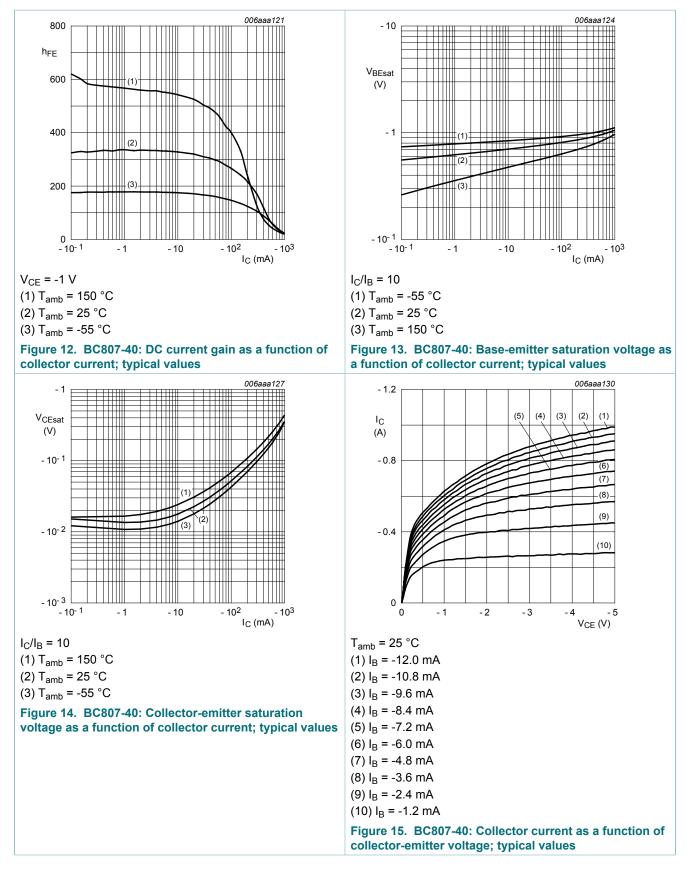
# **BC807 series**



# **BC807** series



# **BC807** series



45 V, 500 mA PNP general-purpose transistors

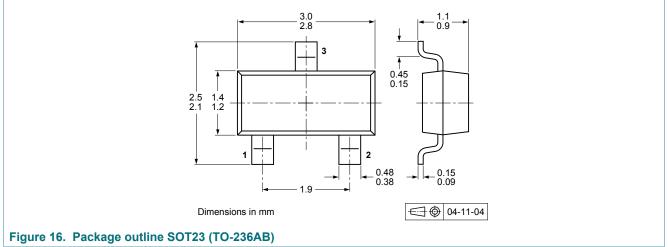
### 8 Test information

### 8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - Stress test qualification for discrete semiconductors, and is suitable for use in automotive applications.

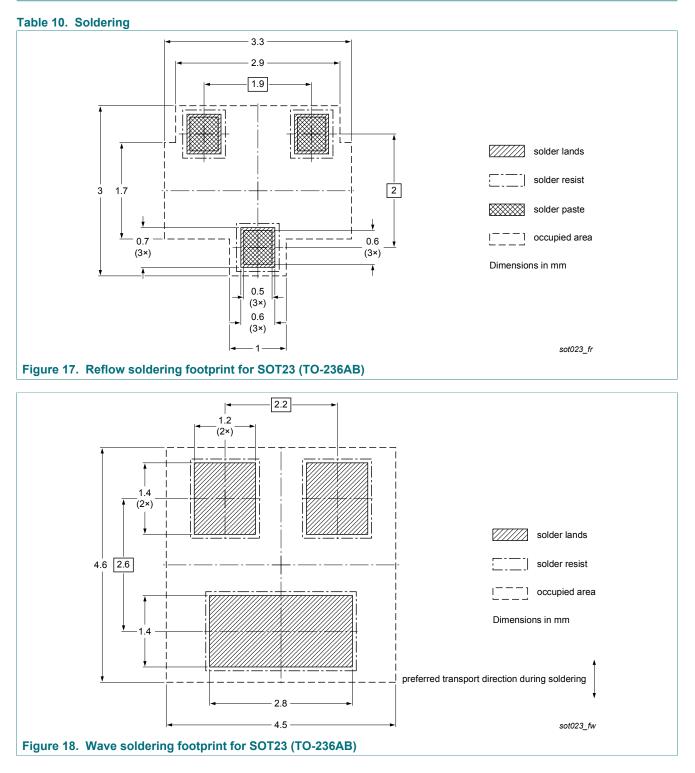
### 9 Package outline

#### Table 9. Package outline



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### **10 Soldering**



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### **11 Revision history**

Table 11. Revision history								
Document ID	Release date	Data sheet status	Change notice	Supersedes				
BC807_SER v.7	20180615	Product data sheet	-	BC807_BC807W_BC327 v.6				
Modifications:	guidelines • Legal tex • Removed • Added Fig thermal g • Graphs in • Added se	s of Nexperia. Is have been adapted to basic types: BC327 ar gures: Fig 1. Power der raphs as Fig 2. and Fig section "Characteristic ctions 8 "Test informati Section "Packing infor	et has been redesigned to comply with the identity oted to the new company name where appropriate. 27 and BC807W (separate data sheet). er derating curves in section "Limiting values" and the id Fig 3. in section "Thermal characteristics". eristics" are sorted in new order. ormation" and 9 "Soldering". information"					
BC807_BC807W_BC327 v.6	20091117	Product data sheet	-	BC807_BC807W_BC327 v.5				
BC807_BC807W_BC327 v.5	20050221	Product data sheet	CPCN200302007F CPCN200405006F	BC807 v.4; BC807W v.3; BC327 v.3				
BC807 v.4	20040116	Product Specification	-	BC807 v.3				
BC807W v.3	19990518	Product Specification	-	BC807W_808W_CNV v.2				
BC327 v.3	19990415	Product Specification	-	BC327 v.2				

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### 12 Legal information

### 12.1 Data sheet status

Document status <sup>[1][2]</sup>	Product status <sup>[3]</sup>	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

Please consult the most recently issued document before initiating or completing a design. [1]

The term 'short data sheet' is explained in section "Definitions".

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BC807 SER **Product data sheet** 

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## **BC807 series**

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